About the Cover
Gary Richardson, AA7VM, wanted a better frequency counter than a previous project. He thought about using a field programmable gate array (FPGA) but realized that a complex programmable logic device (CPLD) would be better suited to the task. The project was an opportunity to learn about this new (to him) class of ICs, so he began reading about them. The resulting frequency counter is described in his article, “Another Frequency Counter for the Experimenter.”

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