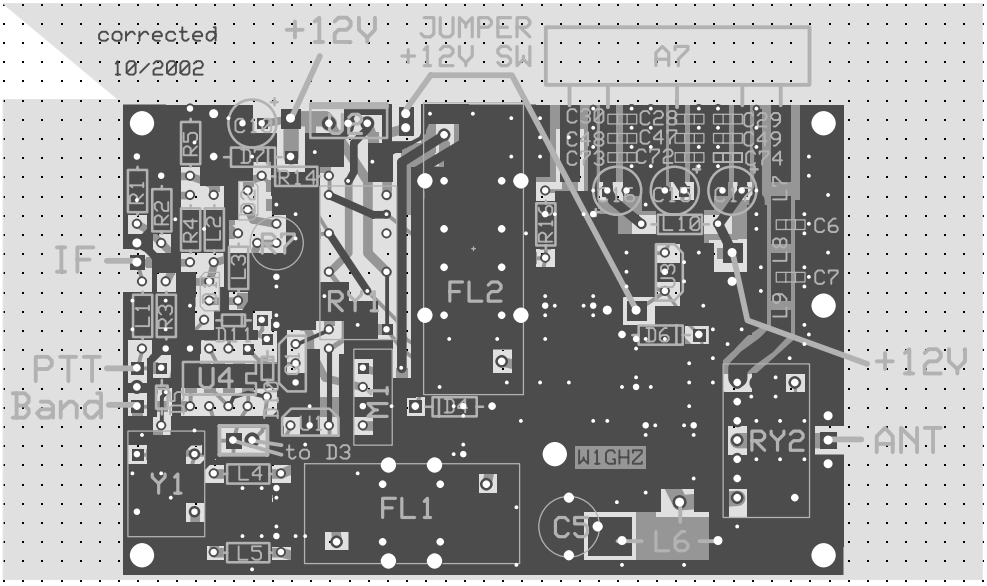

U:\f450\Program Files\LTC\SwCADIII\scratch\222_r2_tref_cor.pcb (Silkscreen, Top layer, Bottom layer)